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(54) DIGITAL SIGNAL RECORDING AND
REPRODUCING DEVICE

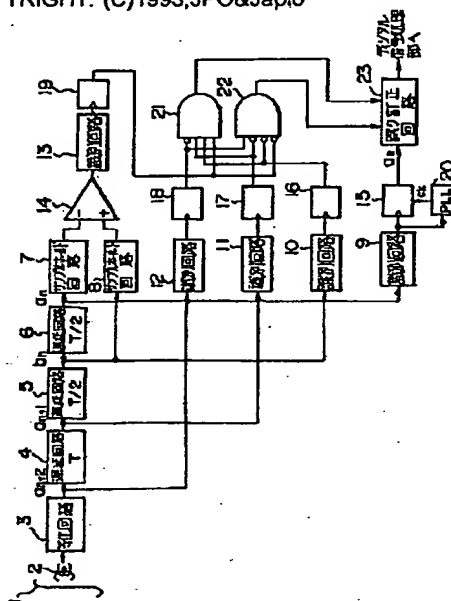
asymmetrical.

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(57) Abstract:

PURPOSE: To reduce erroneous data discrimination at the time of using a vertically oriented tape.

CONSTITUTION: The output of an equalizing circuit 3 is delayed by cascaded delay circuits 4 to 6 to obtain signals at three discrimination points and intermediate points. Discriminating circuits 9 to 12 discriminate signals at respective discrimination points and a discriminating circuit 13 obtains bn-an to detect the inversion part of a code string. An AND circuit 21 operates AND of data of discriminating circuits 10 to 13 and detects a discrimination error in the rise part of a reproduced signal to output an error flag to an error correction circuit 23. An AND circuit 22 operates AND of data of discriminating circuits 10 to 13 and detects a discrimination error in the fall part of the reproduced signal to output an error flag to the error correction circuit 23. When the error flag is inputted, the error correction circuit 23 corrects and outputs discrimination data. Thus, erroneous discrimination is reduced even if an isolated reproduced waveform is



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